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NATIONAL SENIOR CERTIFICATE

GRADE 12

SEPTEMBER 2020

ELECTRICAL TECHNOLOGY: ELECTRONICS

- **MARKS: 200**
- TIME: 3 hours

This question paper consists of 18 pages, including a 2-page formula sheet and 3 answer sheets.

INSTRUCTIONS AND INFORMATION

- 1. This question paper consists of FIVE questions.
- 2. Answer ALL the questions.
- 3. Answer the following questions on the attached ANSWER SHEETS:

QUESTION 4.4.1 QUESTION 4.5.4 QUESTION 4.6.2

- 4. Write your FULL NAME and CLASS SECTION on every ANSWER SHEET and hand them in with your ANSWER BOOK, whether you have used them or not.
- 5. Sketches and diagrams must be large, neat and FULLY LABELLED.
- 6. Show ALL calculations and round off answers correctly to TWO decimal places.
- 7. Number the answers correctly according to the numbering system used in this question paper.
- 8. You may use a non-programmable calculator.
- 9. Calculations must include:
 - 9.1 Formulae and manipulations where needed
 - 9.2 Correct replacement of values
 - 9.3 Correct answer and relevant units where applicable
- 10. A formula sheet is attached at the end of this question paper.
- 11. Write neatly and legibly.

QUESTION 1: OCCUPATIONAL HEALTH AND SAFETY

1.1	Define the term <i>accident,</i> with reference to the Occupational Health and Safety Act, 1993 (Act 85 of 1993).	(2)
1.2	Explain TWO general duties of employees in the workplace.	(2)
1.3	Give TWO unsafe acts in a school workshop that can cause an accident.	(2)
1.4	Briefly explain a <i>third-degree burn</i> .	(2)
1.5	State TWO functions of a health and safety representative.	(2) [10]

QUESTION 2: RLC

2.1.3

Resistive circuit

2.1 Explain the phase relationship between current and voltage in the following AC circuit:

2.1.1	Pure capacitive circuit	(2)
2.1.2	Pure inductive circuit	(2)

2.2 Refer to the circuit diagram FIGURE 2.2 below and answer the questions that follow.





Calculate:

2.2.1	Inductive reactance	(3)
2.2.2	Capacitive reactance	(3)
2.2.3	Impedance of the circuit	(3)
2.2.4	Total current flow through the circuit	(3)

(2)

2.3 Explain how the value of the capacitive reactance will be affected if the supply frequency is increased.

(3)

2.4 A parallel circuit in FIGURE 2.4 consists of a capacitor that draws a current of 4 A, an inductor that draws a current of 6 A and a resistor that draws a current of 5 A. All the components are connected to a 240 V / 50 Hz supply.



FIGURE 2.4: RLC PARALLEL CIRCUIT

Calculate:

	2.4.1	Total current through the circuit	(3)
	2.4.2	The phase angle	(3)
	2.4.3	Inductive reactance	(3)
	2.4.4	Capacitive reactance	(3)
2.5	A para inducto	llel RLC circuit consists of a 10 Ω resistor, a 0,05 μ F and a 0,2 H r connected across a 120 V supply. Calculate the:	
	2.5.1	Resonant frequency	(3)
	2.5.2	Q-factor	(4)
	2.5.3	Bandwidth	(3)
2.6	A serie bandwi coil has	es RLC tuned circuit has a resonant frequency of 95 MHz and a dth of 200 kHz. The capacitor is 2,5 pF and the wire used to wind the s zero resistance. Calculate:	
	2.6.1	The Q factor of the circuit	(3)
	2.6.2	The size of the inductor	(2)
	2.6.3	The circuit resistance	(2) [50]

QUESTION 3: SEMICONDUCTORS

3.1	Explain the purpose of a field effect transistor.	(2)
3.2	State the main advantage of field-effect devices over bipolar junction devices.	(4)
3.3	Draw and label a symbol for the P-channel enhancement MOSFET.	(3)
3.4	State TWO applications of an unijunctiontransistor (UJT).	(2)
3.5	List THREE ideal properties of an ideal Op Amp.	(3)
3.6	Describe the differences between the inverting and non-inverting inputs of an Op amp.	(2)
3.7	Draw and label the symbol of a Darlington pair transistor.	(4)
3.8	Explain the advantage of using negative feedback with an Op amp.	(3)

3.9 Refer to FIGURE 3.9 and calculate the gain of the amplifier if the feedback resistor has a value of 120 k Ω and input resistor has a value of 12 k Ω .



3.10 Refer to FIGURE 3.10 below and answer the questions that follow.



			[40]
3.13	Explain 555 IC.	the purpose of the THREE internally connected series resistor in a	(3)
3.12	List THREE modes of operation of a 555 timer IC.		(3)
3.11	Explain	the purpose of the comparator.	(2)
	3.10.2	Calculate the output voltage.	(3)
	3.10.1	Calculate the gain of the Op amp.	(3)

QUESTION 4: SWITCHING CIRCUITS

- 4.1 Explain the purpose of an astable multivibrator. (2)
- 4.2 Draw a fully labelled circuit diagram of a 741 astable multivibrator circuit. (5)
- 4.3 Refer to FIGURE 4.3 below and answer the questions that follow.



FIGURE 4.3: BISTABLE MULTIVIBRATOR

4.3.1	State the function of R_1 and R_2 .	(2)
4.3.2	Describe what happens when the set switch, S_1 is pressed.	(3)
4.3.3	Explain why threshold pin 6 is connected directly to ground.	(3)

4.4 FIGURE 4.4 below shows input trigger pulses **A** and **B** to a 555 monostable multivibrator. Answer the questions that follow.



FIGURE 4.4: MONOSTABLE MULTIVIBRATOR TRIGGER PULSE

- 4.4.1 Draw the output signal on ANSWER SHEET 4.4.1. (4)
- 4.4.2 Describe the condition that occurred at trigger pulse **B**. (2)
- 4.4.3 Explain why the condition that occured at trigger pulse **B** does NOT affect the capacitor charging. (3)

4.5 Refer to FIGURE 4.5.1 below and answer the questions that follow.



FIGURE 4.5.1: SCHMITT TRIGGER INPUT AND OUTPUT

- 4.5.1 State whether the output signal represents an inverting or noninverting Schmitt trigger. Motivate your answer.
- 4.5.2 Draw the circuit diagram of a Schmitt trigger consisting of TWO resistors and a 741 op-amp that will produce the output signal in FIGURE 4.5.1.

4.5.3 Name TWO transducers that can be used as input devices to the Schmitt trigger. (2)

4.5.4 Draw the output signal on ANSWER SHEET 4.5.4 if the input signal in FIGURE 4.5.4 below is applied to the circuit.



(3)

(7)

(4)

4.6 Refer to FIGURE 4.6 below and answer the questions that follow.



- 4.6.1 Identify the op-amp circuit diagram in FIGURE 4.6. (2)
- 4.6.2 Draw the output signal on the ANSWER SHEET for QUESTION 4.6.2.
- 4.6.3 Explain how the reference voltage is made negative. (2)
- 4.7 Refer to FIGURE 4.7 below and answer the questions that follow.





Given:

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4.7.1	Explain how the gain of this amplifier can be determined.	(3)
4.7.2	Calculate the output voltage of the amplifier.	(3)
4.7.3	Give a reason why the amplifier is NOT saturated.	(2)
4.7.4	Calculate the gain of the amplifier using voltage values.	(3)
4.7.5	Describe what will happen to the output voltage if the value of R_2 is changed to 10 $k\Omega.$	(1) [60]

QUESTION 5: AMPLIFIERS

Refer to FIGURE 5.1 below and answer the following questions. 5.1



FIGURE 5.1

Calculate:

	5.1.1	The voltage across the resistor (VCE)	(2)
	5.1.2	The maximum current	(3)
5.2	Explai	n TWO advantages of class AB amplifiers.	(2)
5.3	A 741 operational amplifier has an open loop gain of 100. It is connected into a negative feedback circuit which feeds back 9% of the output signal. Answer the following questions.		
	5.3.1	Draw the circuit diagram of a negative feedback.	(5)
	5.3.2	Name THREE advantages of negative feedback	(3)
	5.3.3	Calculate the overall gain of negative feedback.	(3)
5.4	State	TWO advantages of a transformer coupled amplifier.	(2)
5.5	List T\	NO uses for RF amplifiers.	(2)
5.6	State TWO advantages of a push pull amplifier. (2		(2)

(2)

(6)

(2)

(3)

5.7	Refer	to an RC-phase shift oscillator and answer the following questions.
	5.7.1	Write down the formula to calculate the resonant frequency of a phase shift oscillator.
	5.7.2	Draw a circuit diagram of a RC phase shift oscillator.
	5.7.3	State TWO advantages of the RC network.
	5.7.4	Explain the type of feedback used by the RC phase shift oscillator.

- 5.7.5 List THREE important areas on a frequency response curve. (3)
 - [40]

TOTAL: 200

FORMULA SHEET: ELECTRICAL TECHNOLOGY SEMI-CONDUCTORS DEVICES **RLC CIRCUIT** $A_V = \frac{Vout}{Vin} = \frac{R_F}{R_{IN}}$ $XL = 2\pi FL$ and $XC = \frac{1}{2\pi FC}$ $V_{OUT} = V_{IN} = \left(-\frac{R_F}{R_{IN}}\right)$ SERIES $A_V = 1 + \frac{R_F}{R_{IN}}$ $I_T = I_R = I_C = I_L$ $V_{OUT} = V_{IN} \left(1 + \frac{R_F}{R_{IN}} \right)$ $Z = \sqrt{R^2 + (X_L - X_C)^2}$ $\beta_{super} = \beta_1 \times \beta_2$ $VT = \sqrt{{V_R}^2 + (V_{L-}V_C)^2}$ $VL = IX_L$ and $Vc = IX_C$ and $V_T = IZ$ AMPLIFIERS $COS\theta = \frac{R}{Z}$ $V_{CE} = V_{CC}$ $I_{Cmax} = \frac{V_{CC}}{R_C}$ $COS\theta = \frac{V_R}{V_T}$ $A' = \frac{A}{1+\beta A}$ $Q = \frac{X_L}{Z} = \frac{X_C}{Z} = \frac{V_L}{V_S} = \frac{V_C}{V_S} = \frac{1}{R} \sqrt{\frac{L}{C}}$ Power Gain $A_P = \log_{10} \left(\frac{P_{out}}{P_{in}} \right)$ PARALLEL $A_V = 20 \log_{10} \frac{E_{out}}{E_{in}} dB$ 1. $VT = V_R = V_L = V_C$ 2. $I_R = \frac{V}{R} = and I_L = \frac{V}{X_L} = I_C = \frac{V}{X_C}$ $A_I = 20 \log_{10} \frac{I_{out}}{I_{in}}$ $F_0 = \frac{1}{2\pi\sqrt{L_T C}}$ 3. $I_T = \sqrt{{I_R}^2 + (I_L - I_C)^2}$ $Fr = \frac{1}{2\pi\sqrt{LC}}$ 4. $COS\theta = \frac{I_R}{I_T}$ $F_0 = \frac{1}{2\pi\sqrt{6RC}}$ 5. $Q = \frac{X_L}{Z} = \frac{X_C}{Z} = \frac{V_L}{V_S} = \frac{1}{R} = \sqrt{\frac{L}{C}}$

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SWITCHING CIRCUITS

1. Gain
$$A_V = \frac{V_{OUT}}{V_{IN}} = -\left(\frac{R_f}{R_{in}}\right)$$
 inverting operational amplifier

2. Gain
$$A_V = \frac{V_{OUT}}{V_{IN}} = 1 + \left(\frac{R_f}{R_{in}}\right)$$
 non-inverting operational amplifier

3.
$$V_{OUT} = V_{IN} \times \left(-\frac{R_f}{R_{in}}\right)$$
 inverting amplifier

4.
$$V_{OUT} = -(V_1 + V_2 + V_3)$$
 summing up op-amp

5.
$$f_r = \frac{1}{2\pi\sqrt{LC}}$$

$$6. \qquad f = \frac{1}{2\pi\sqrt{6RC}}$$

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ANSWER SHEET 4.4.1

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ANSWER SHEET 4.5.4

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NAME:	



ANSWER SHEET 4.6.2

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